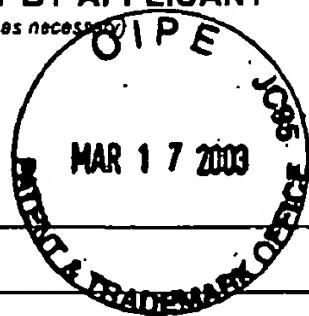


Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/751,610
Filing Date	December 29, 2000
First Named Inventor	Harris, William
Group Art Unit	2816
Examiner Name	Cassandra F. Cox

Sheet 1 of 1

Attorney Docket No: H16-26054 (256.058US1)

US PATENT DOCUMENTS

Examiner Initials*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
TL	US-4,282,493	08/04/1981	Moreau, D. L.	331	2	07/02/1979

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
TL	DE-4214612	04/11/1993	Suwald, T.	H03K	23/40	
TL	EP-0526227	02/03/1993	Fujii, T.	H03L	7/87	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TL		HAQUE, S. E., et al., "Phase-Locked Loop Based Linearly Controlled Three-Phase Firing Circuit for an AC Controller", Int. J. Electronics, 58, (1985), 761-767	
TL		SPARKES, R., et al., "Evaluation of Macro Models for Mixed Analog/Digital Circuits", Proceedings of the IEEE 1998 Custom Integrated Circuits Conference, Rochester, NY, (05 16 1988), 3.4.1 - 3.4.6	

RECEIVED
MAR 20 2003
TECHNOLOGY CENTER 2800

EXAMINER

DATE CONSIDERED

3/10/2003